

## ABSTRACT

A method and associated apparatus are provided for improving the performance of a high speed data bus, such as a memory bus, using selectively activated receiver and driver pairs. Each receiver and driver pair may be selectively activated to permit data communication on a segment of the high speed data bus coupled to the activated receiver and driver pair. Each receiver and driver pair may also be deactivated, thereby disconnecting at least a respective segment of the high speed data bus, so that communicating system components may be connected in a substantially stubless environment.